

1)

Sentsoreak: motorea martxan edo geldi dagoen detektatzen da (A), Segurtasun uhala lotuta dagoen a al ez detektatzen dueña (B).

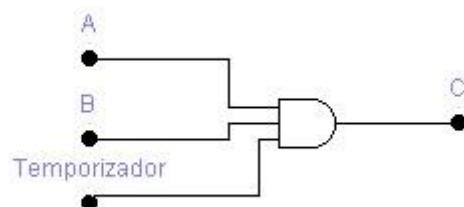
Alarma katibatzeko (C)

A = 1 Motorea martxan

B= 1 ez-lotuta

C = 1 Alarma aktibatu behar da

Temporizadorea= 30 segundu

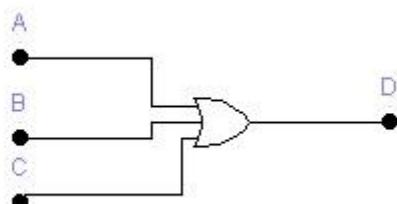


2) Sentsoreak: Lehia edo atea zabalik dauden (A, B, C)

Eragingailua: Alarma (D)

A=B=C=1 atea edo lehioa zabalik

D=1 arrotza detektatu da



3)

$$③ \text{ a) } f_1(ABCD) = \bar{A}\bar{C} + A\bar{C}D + \bar{A}B\bar{C}D$$

$$f_1 = \bar{A}\bar{C}D + \bar{A}\bar{C}\bar{D} + (\bar{A}B\bar{C}D + A\bar{B}\bar{C}D) + \bar{A}B\bar{C}D$$

$$f_1 = \begin{matrix} \bar{A}B\bar{C}D & + & \bar{A}\bar{B}\bar{C}D & + & \bar{A}B\bar{C}\bar{D} & + & \bar{A}\bar{B}\bar{C}\bar{D} & + & AB\bar{C}D & + & A\bar{B}\bar{C}D & + & \bar{A}BCD \\ 0101 & + & 0001 & & 0100 & & 0000 & & 1101 & & 1001 & & 0111 \end{matrix}$$

$$f_1 = 5 + 1 + 4 + 0 | + 13 + 9 + 7$$

$$f = \sum (0, 4, 5, 7, 1, 13) = \sum (0, 1, 4, 5, 7, 9, 13)$$

$$\text{b) } f_2 = \bar{A}\bar{C}\bar{D} + AC + ABC$$

$$f = \bar{A}B\bar{C}\bar{D} + \bar{A}\bar{B}\bar{C}\bar{D} + (ABC + A\bar{B}C) + (ABCD + ABC\bar{D})$$

$$f = \begin{matrix} \bar{A}B\bar{C}\bar{D} & + & \bar{A}\bar{B}\bar{C}\bar{D} & + & ABCD & + & ABC\bar{D} & + & A\bar{B}CD & + & A\bar{B}C\bar{D} & + & ABC\bar{D} \\ 0100 & + & 0000 & & 1111 & & 1110 & & 1011 & & 1010 & & 1111 & & 1110 \\ 4 & & 0 & & 15 & & 14 & & 11 & & 10 & & 15 & & 14 \end{matrix}$$

$$f = \sum (0, 4, 10, 11, 14, 15)$$

$$\text{c) } f_3 = \bar{A}B + AC$$

$$f = \bar{A}BC + \bar{A}\bar{B}\bar{C} + ABC + A\bar{B}C$$

$$f = \sum (2, 3, 5, 7)$$

$$\begin{aligned} (A+B+\bar{D}) &= (A+0+0+1)(A+0+1+1) \\ (A+\bar{B}+\bar{D}) &= (A+1+0+1)(A+1+1+1) \\ &\quad \uparrow \qquad \uparrow \\ &\quad 1, 3, 5, 7 \end{aligned}$$

$$\text{d) } f_4 = (\bar{B}+\bar{D})(\bar{B}+A)(C+\bar{D})(\bar{D}+A) = \sum (1, 3, 4, 5, 6, 7, 9, 13, 15)$$

$$(\bar{B}+\bar{D}) = (\bar{A}+\bar{B}+\bar{D})(A+\bar{B}+\bar{D})$$

$$\begin{cases} (\bar{A}+\bar{B}+\bar{D}) = (\bar{A}+\bar{B}+0+1)(\bar{A}+0+1+1) \\ (\bar{A}+\bar{B}+\bar{D}) = (A+1+0+1)(A+1+1+1) \end{cases} \Rightarrow 15, 13, 7, 5,$$

$$(A+\bar{B}) = (A+\bar{B}+\bar{C})(A+\bar{B}+C)$$

$$\begin{cases} A+\bar{B}+\bar{C} = (A+\bar{B}+0+1)(A+0+1+1) \\ A+\bar{B}+C = (A+\bar{B}+0+1)(A+0+1+0) \end{cases} \Rightarrow 7, 6, 5, 4$$

$$A+\bar{B}+C = (A+\bar{B}+0+1)(A+0+1+0)$$

$$(C+\bar{D}) = (A+C+\bar{D})(\bar{A}+C+\bar{D})$$

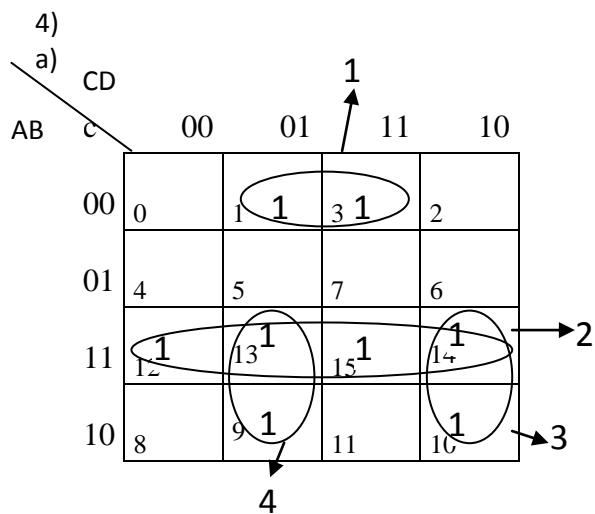
$$\begin{cases} A+C+\bar{D} = (A+0+C+1)(A+1+C+1) \\ \bar{A}+C+\bar{D} = (\bar{A}+0+C+1)(\bar{A}+1+C+1) \end{cases} \Rightarrow 1, 5, 9, 13$$

$$\bar{A}+C+\bar{D} = (\bar{A}+0+C+1)(\bar{A}+1+C+1)$$

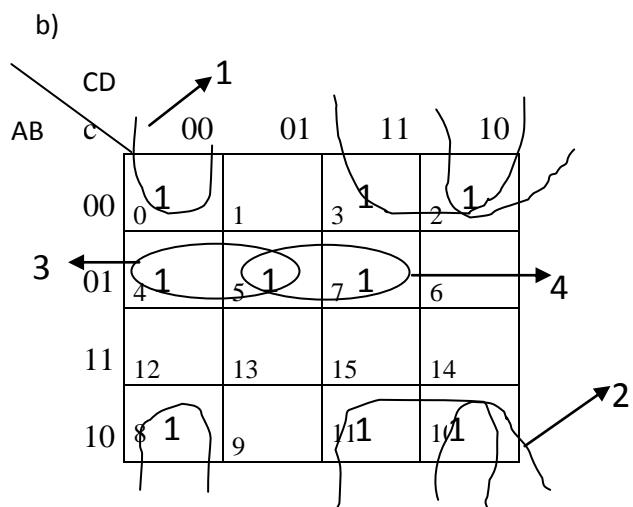
$$\left\{ \begin{array}{l} \bar{x}_1 x_2 = \bar{x}_1 x_2 x_3 + \bar{x}_1 x_2 \bar{x}_3 \\ \bar{x}_1 x_2 x_3 = \bar{x}_1 x_2 x_3 x_4 + \bar{x}_1 x_2 x_3 \bar{x}_4 \\ \quad \rightarrow \begin{matrix} \bar{x}_1 & x_2 & x_3 & x_4 & \bar{x}_5 \\ 0 & 1 & 1 & 1 & 0 \end{matrix} + \begin{matrix} \bar{x}_1 & x_2 & x_3 & x_4 & x_5 \\ 0 & 1 & 1 & 1 & 1 \end{matrix} \\ \quad \rightarrow \begin{matrix} \bar{x}_1 & x_2 & x_3 & \bar{x}_4 & \bar{x}_5 \\ 0 & 1 & 1 & 0 & 0 \end{matrix} + \begin{matrix} \bar{x}_1 & x_2 & x_3 & \bar{x}_4 & x_5 \\ 0 & 1 & 1 & 0 & 1 \end{matrix} \cancel{\downarrow} \\ \bar{x}_1 x_2 \bar{x}_3 = \bar{x}_1 x_2 \bar{x}_3 x_4 + \bar{x}_1 x_2 \bar{x}_3 \bar{x}_4 \\ \quad \rightarrow \begin{matrix} \bar{x}_1 & x_2 & \bar{x}_3 & x_4 & \bar{x}_5 \\ 0 & 1 & 0 & 1 & 0 \end{matrix} + \begin{matrix} \bar{x}_1 & x_2 & \bar{x}_3 & x_4 & x_5 \\ 0 & 1 & 0 & 1 & 1 \end{matrix} \quad 14, 15, 12, 13 \\ \quad \rightarrow \begin{matrix} \bar{x}_1 & x_2 & \bar{x}_3 & \bar{x}_4 & \bar{x}_5 \\ 0 & 1 & 0 & 0 & 0 \end{matrix} + \begin{matrix} \bar{x}_1 & x_2 & \bar{x}_3 & \bar{x}_4 & x_5 \\ 0 & 1 & 0 & 0 & 1 \end{matrix} \end{array} \right.$$

$$\left\{ \begin{array}{l} \bar{x}_1 \bar{x}_3 = \bar{x}_1 x_2 \bar{x}_3 + \bar{x}_1 \bar{x}_2 \bar{x}_3 \\ \bar{x}_1 x_2 \bar{x}_3 = \bar{x}_1 \bar{x}_2 \bar{x}_3 x_4 + \bar{x}_1 x_2 \bar{x}_3 \bar{x}_4 \\ \quad \rightarrow \begin{matrix} \bar{x}_1 & \bar{x}_2 & \bar{x}_3 & x_4 & \bar{x}_5 \\ 0 & 1 & 0 & 1 & 0 \end{matrix} + \begin{matrix} \bar{x}_1 & x_2 & \bar{x}_3 & \bar{x}_4 & x_5 \\ 0 & 1 & 0 & 1 & 0 \end{matrix} \\ \quad \rightarrow \begin{matrix} \bar{x}_1 & x_2 & \bar{x}_3 & x_4 & \bar{x}_5 \\ 0 & 1 & 0 & 1 & 1 \end{matrix} + \begin{matrix} \bar{x}_1 & x_2 & \bar{x}_3 & \bar{x}_4 & x_5 \\ 0 & 1 & 0 & 1 & 1 \end{matrix} \quad 8, 9, 10, 11 \\ \bar{x}_1 \bar{x}_2 x_3 = \bar{x}_1 \bar{x}_2 x_3 x_4 + \bar{x}_1 \bar{x}_2 x_3 \bar{x}_4 \\ \quad \rightarrow \begin{matrix} \bar{x}_1 & \bar{x}_2 & x_3 & x_4 & \bar{x}_5 \\ 0 & 1 & 0 & 1 & 0 \end{matrix} + \begin{matrix} \bar{x}_1 & \bar{x}_2 & x_3 & \bar{x}_4 & x_5 \\ 0 & 1 & 0 & 1 & 1 \end{matrix} \quad 4, 5, 6, 7 \\ \quad \rightarrow \begin{matrix} \bar{x}_1 & \bar{x}_2 & x_3 & x_4 & \bar{x}_5 \\ 0 & 1 & 0 & 1 & 0 \end{matrix} + \begin{matrix} \bar{x}_1 & \bar{x}_2 & x_3 & x_4 & x_5 \\ 0 & 1 & 0 & 1 & 1 \end{matrix} \end{array} \right.$$

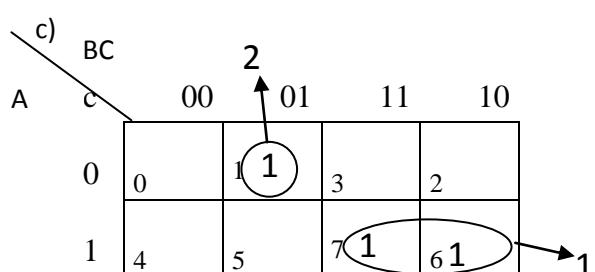
$$\left\{ \begin{array}{l} x_2 x_4 = x_1 x_2 x_4 + \bar{x}_1 x_2 x_4 \\ x_1 x_2 x_4 = x_1 x_2 x_3 x_4 + x_1 x_2 \bar{x}_3 x_4 \\ \quad \rightarrow \begin{matrix} x_1 & x_2 & x_3 & x_4 & \bar{x}_5 \\ 1 & 1 & 1 & 0 & 0 \end{matrix} + \begin{matrix} x_1 & x_2 & x_3 & x_4 & x_5 \\ 1 & 1 & 1 & 0 & 1 \end{matrix} \\ \quad \rightarrow \begin{matrix} x_1 & x_2 & \bar{x}_3 & x_4 & \bar{x}_5 \\ 1 & 1 & 0 & 0 & 0 \end{matrix} + \begin{matrix} x_1 & x_2 & \bar{x}_3 & x_4 & x_5 \\ 1 & 1 & 0 & 0 & 1 \end{matrix} \quad 30, 31, 26, 27 \\ \bar{x}_1 x_2 x_4 = \bar{x}_1 x_2 x_3 x_4 + \bar{x}_1 x_2 \bar{x}_3 x_4 \\ \quad \rightarrow \begin{matrix} \bar{x}_1 & x_2 & x_3 & x_4 & \bar{x}_5 \\ 0 & 1 & 0 & 1 & 0 \end{matrix} + \begin{matrix} \bar{x}_1 & x_2 & \bar{x}_3 & x_4 & x_5 \\ 0 & 1 & 0 & 1 & 1 \end{matrix} \quad 14, 15, 10, 11 \\ \quad \rightarrow \begin{matrix} \bar{x}_1 & x_2 & x_3 & x_4 & \bar{x}_5 \\ 0 & 1 & 0 & 1 & 0 \end{matrix} + \begin{matrix} \bar{x}_1 & x_2 & \bar{x}_3 & x_4 & x_5 \\ 0 & 1 & 0 & 1 & 1 \end{matrix} \end{array} \right.$$



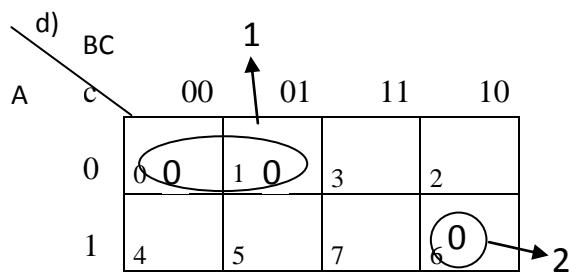
$$f = \overline{A} \overline{B} D + AB + A\overline{C}\overline{D} + A\overline{C}D$$



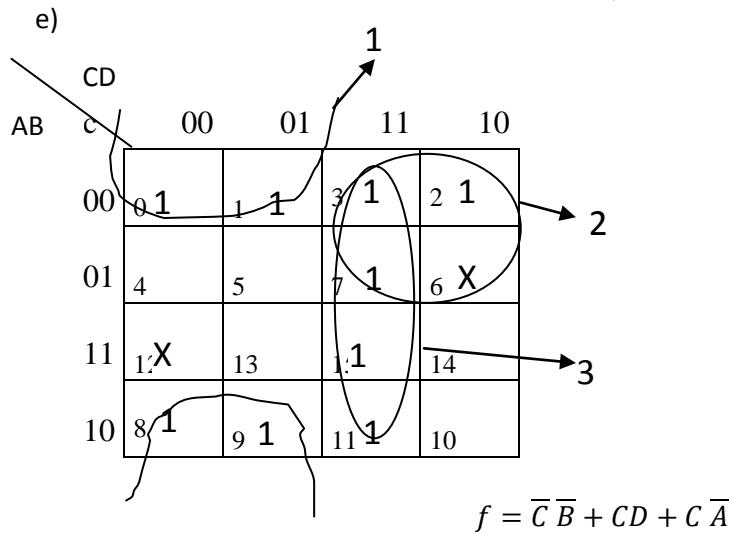
$$f = \overline{B}C + \overline{D}\overline{B} + \overline{A}B\overline{C} + \overline{A}BD$$



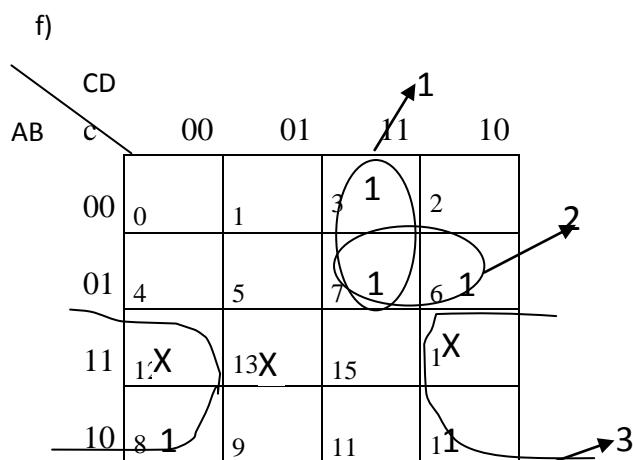
$$f = AB + \overline{A}\overline{B}C$$



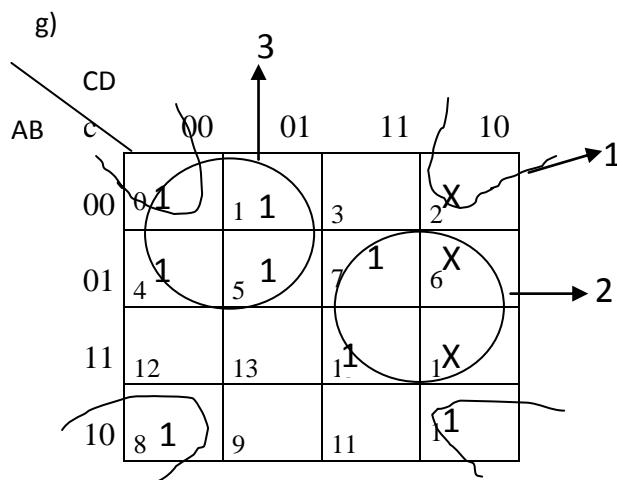
$$f = (A + B)(\bar{A} + \bar{B} + C)$$



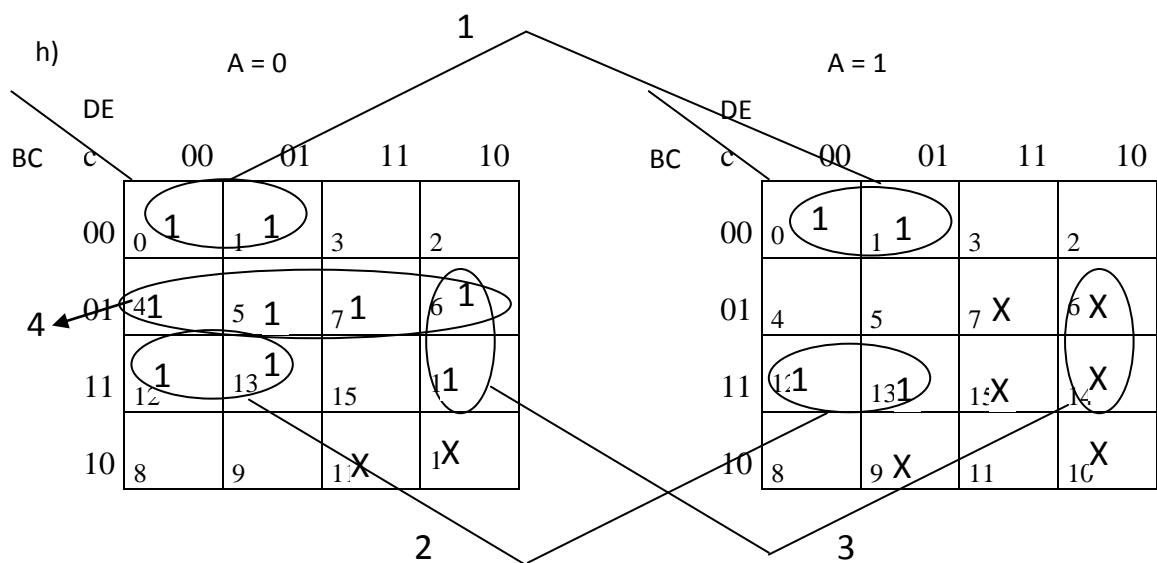
$$f = \bar{C}\bar{B} + CD + C\bar{A}$$



$$f = A\bar{D} + \bar{A}BC + \bar{A}CD$$



$$f = A\bar{C} + BC + \bar{B}\bar{D}$$



$$f = \overline{DCB} + BC\bar{D} + CDE\bar{E} + \overline{A}\overline{B}C$$

5)

$$⑤ a) f = ABC + DE$$

$$f = \overline{\overline{ABC} \cdot \overline{DE}} = \overline{\overline{AB} \cdot \overline{C} \cdot \overline{DE}}$$

$$b) f = \overline{\overline{ABC} + \overline{D} + \overline{E}}$$

$$f = \overline{\overline{ABC} \cdot \overline{D} \cdot \overline{E}} = \overline{\overline{\overline{AB} \cdot \overline{C}} \cdot \overline{D} \cdot \overline{E}} = \overline{\overline{\overline{AB} \cdot \overline{C}}} \cdot \overline{\overline{DE}}$$

$$c) f = \overline{\overline{ABC}} = \overline{\overline{\overline{AB}} \cdot \overline{C}}$$

$$d) f = \overline{\overline{AB}} + \overline{\overline{CD}} = \overline{\overline{AB}} \cdot \overline{\overline{CD}}$$

$$e) f = (\overline{\overline{A+B}}) (\overline{\overline{C+D}})$$

$$f = (\overline{\overline{A}} \cdot \overline{\overline{B}}) (\overline{\overline{C}} \cdot \overline{\overline{D}}) = \overline{\overline{\overline{A} \cdot \overline{B}}} \cdot \overline{\overline{\overline{C} \cdot \overline{D}}}$$

$$f) f = AB [C(\overline{DE} + \overline{AB}) + \overline{BCE}]$$

$$f = \overline{\overline{AB}} [C(\overline{\overline{DE} + \overline{AB}}) + \overline{BCE}]$$

$$f = \overline{\overline{AB}} [C(\overline{DE} \cdot \overline{AB}) + \overline{BCE}]$$

$$f = \overline{\overline{AB}} [\overline{C \cdot \overline{DE} \cdot \overline{AB}} + \overline{BCE}]$$

$$f = \overline{\overline{AB}} [\overline{C \cdot \overline{DE} \cdot \overline{AB}} \cdot \overline{BCE}]$$

$$f = \overline{\overline{AB}} [\overline{C \cdot \overline{DE} \cdot \overline{AB}} \cdot \overline{\overline{BCE}}]$$

$$g) f = B(C\overline{DE} + \overline{EFG})(\overline{AB} + C)$$

$$f = B (\overline{C\overline{DE} + \overline{EFG}}) \cdot (\overline{\overline{AB} + C})$$

$$f = B (\overline{C\overline{DE} \cdot \overline{EFG}}) \cdot (\overline{\overline{AB} \cdot \overline{C}})$$

$$f = B (\overline{C\overline{DE} \cdot \overline{EFG}}) \cdot (\overline{\overline{AB} \cdot \overline{C}})$$

6)

	TTL	TTL LS	TTL ALS	CMOS
MR_H	0.4	0.7	0.7	1
MR_L	0.4	0.3	0.4	1
FO_H	10	20	20	72000 ¹
FO_L	10	22	20	176000 ²
FO	10	20	20	<50 ³

$$*1: I_{IH}: 0.005\mu A \rightarrow I_{OH}: n. I_{IH} \rightarrow n = 360 / 0.005 = 72000$$

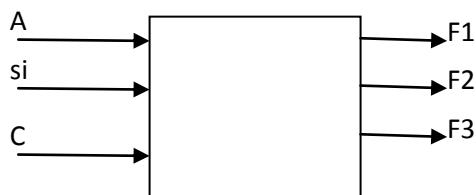
$$I_{OH}: 360 \mu A$$

$$*2: I_{OL}: n. I_{IL} \rightarrow n = 0.88 \cdot 10^{-3} / 0.005 \cdot 10^{-6} = 176 \cdot 10^3$$

*3: CMOS-ak FO altua du, baina ateen sarreren kapazitantziak direla eta (karga eta deskarga denborak, noski), mugatuta dago FO-a.

7) Abiadura, zaratarekiko inmunitatea eta disipazio ezaugarriak TTL LS edo ALS ateetan hobe dira. Alabaina, irteera-korrонteak txikiagoak direnez aztertu beharko litzateke zenbat sarrera elika ditzakeen TTL irteera estandarra eta konparatu LS eta ALS-rekin.

8)



A, B, C = sentsoreen irteera = 1 kotxea dagoenean.

F1, F2, F3 = semaforoak aktibatzeko seinalea = 1 semaforoa gorri z jarri.

Sarrerak			Irteerak		
A	B	C	F1	F2	F3
0	0	0	1	1	1
0	0	1	1	1	0
0	1	0	1	0	1
0	1	1	1	0	1
1	0	0	0	1	1
1	0	1	0	1	1
1	1	0	0	1	1
1	1	1	0	1	1

$$\begin{aligned}
 f_1(ABC) &= \Sigma(0, 1, 2, 3) = \bar{A}\bar{B}\bar{C} + \bar{A}\bar{B}C + \bar{A}B\bar{C} + \bar{A}BC = \\
 &= \bar{A}\bar{B}(\bar{C}+C) + \bar{A}B(\bar{C}+C) = \bar{A}\bar{B} + \bar{A}B = \bar{A}(\bar{B}+B) \\
 &= \bar{A}
 \end{aligned}$$

$$\begin{aligned}
 f_2(ABC) &= \Pi(2, 3) = (A+\bar{B}+C) \cdot (\bar{A}+\bar{B}+\bar{C}) = A+\bar{B}+C \cdot \bar{C} = \\
 &= A+\bar{B}+0 = A+\bar{B}
 \end{aligned}$$

	BC	00	01	11	10
A	0	10	11	12	12
	1	1	1	1	0

$$f_1 = \bar{A}$$

	BC	00	01	11	10
A	0	10	11	12	12
	1	14	11	17	16

$$f_2 = \bar{B} + A$$

	BC	00	01	11	10
A	0	10	11	13	12
	1	14	15	12	16

$$f_3 = \bar{C} + A + B$$

9) A, B, C, D = etengailuen irteera = 1 alde.

F = LED-a aktibatzeko seinalea = 1 proposamena onartzen da (> 50%)

A	B	C	D	F
0	0	0	0	0
0	0	0	1	0
0	0	1	0	0
0	0	1	1	0
0	1	0	0	0
0	1	0	1	0
0	1	1	0	1
0	1	1	1	1
1	0	0	0	0
1	0	0	1	0
1	0	1	0	1
1	0	1	1	1
1	1	0	0	1
1	1	0	1	1
1	1	1	0	1
1	1	1	1	1

$$f(ABCD) = \sum (6, 7, 10, 11, 12, 13, 14, 15)$$

$$\begin{aligned} f &= \bar{A}BC\bar{D} + \bar{A}B\bar{C}D + A\bar{B}CD + A\bar{B}\bar{C}\bar{D} \\ &\quad + AB\bar{C}\bar{D} + AB\bar{C}D + ABC\bar{D} + ABCD \end{aligned}$$

$$\begin{aligned} f &= \bar{A}BC(\bar{D}+D) + A\bar{B}C(\bar{D}+D) + \\ &\quad + AB\bar{C}(\bar{D}+D) + ABC(\bar{D}+D) \end{aligned}$$

$$f = \bar{A}BC + A\bar{B}C + AB\bar{C} + ABC$$

$$f = \bar{A}BC + A\bar{B}C + AB$$

$$f = \bar{A}BC + A(\underbrace{\bar{B}C+B}_{B+C})$$

$$f = \bar{A}BC + A(B+C)$$

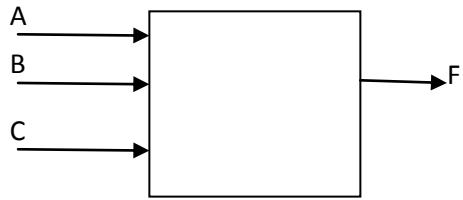
$$f = \bar{A}BC + AB + AC$$

$$f = B(\underbrace{\bar{A}C+A}_{A+C}) + AC$$

$$f = B(A+C) + AC = \underline{BA} + \underline{BC+AC}$$

AB		CD			
		00	01	11	10
00	0	1	3	2	
	4	5	1	6	
01		12	13	15	14
		8	9	11	10

10)



A, B, C = sentsoreen irteea = 1 puntuaren azpitik baldin badago

F = alarma aktibatzeko seinalea = 1 alarma dago.

A	B	C	F
0	0	0	0
0	0	1	0
0	1	0	0
0	1	1	1
1	0	0	0
1	0	1	1
1	1	0	1
1	1	1	1

$$f(ABC) = \sum(3, 5, 6, 7)$$

$$f = \bar{A}BC + A\bar{B}C + \underbrace{ABC}_{AB\bar{C}} + A\bar{B}C$$

$$f = \bar{A}BC + A\bar{B}C + AB$$

$$f = \bar{A}BC + A(\bar{B}C + B) =$$

$$f = \bar{A}BC + A(C + B) =$$

$$f = \bar{A}BC + AC + AB$$

$$f = B(\bar{A}C + A) + AC$$

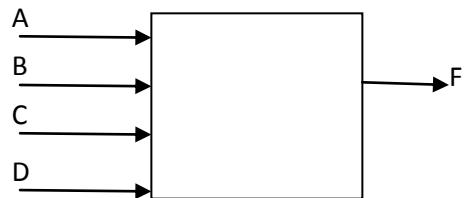
$$f = B(A + C) + AC$$

$$\boxed{f = BA + BC + AC}$$

		00	01	11	10
		0	0	1	1
A	B	0	0	1	1
		1	1	1	1

$$f = AB + BC + AC$$

11)



A, B, C, D = sarrera-aldagaiak.

F = irteera aldagaiak

A	B	C	D	F
0	1	1	1	1
1	0	1	1	1
1	1	0	1	1
1	1	1	0	1

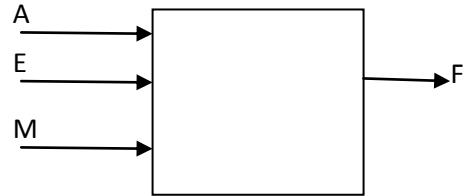
$$\begin{cases} f(ABCD) = \bar{A}\bar{B}CD + A\bar{B}CD + AB\bar{C}D + \\ + ABC\bar{D} \end{cases}$$

$$f = (\bar{A}B + A\bar{B}) \cdot CD + AB(\bar{C}D + C\bar{D})$$

$$\boxed{f = (A \oplus B) \cdot CD + AB(C \oplus D)}$$

CD	00	01	11	10
AB	00	1	2	2
CD	00	01	11	10
AB	00	01	11	10
CD	00	01	11	10

12)



A = atea zabalik ala itxita dagoen asierazteko = 1 zabalik

E = eskaera dagoen adierazteko = 1 eskaera dago

M = mugimendua dagoen adierazteko = 1 mugimendua dago

F = funtzionamenduan akatsik egon den alarma aktibatzeko seinalea = 1 alarma

A	M	E	F
0	0	0	1
0	0	1	1
0	1	0	1
0	1	1	0
1	0	0	0
1	0	1	1
1	1	0	1
1	1	1	1

$$F(A, M, E) = \bar{A}\bar{M}\bar{E} + \bar{A}\bar{M}E + \bar{A}M\bar{E} +$$

$$+ A\bar{M}E + A\bar{M}\bar{E} + AM\bar{E}$$

$$F = \bar{A}\bar{M}(\bar{E} + E) + (\bar{A} + A)M\bar{E} + AE(\bar{M} + M)$$

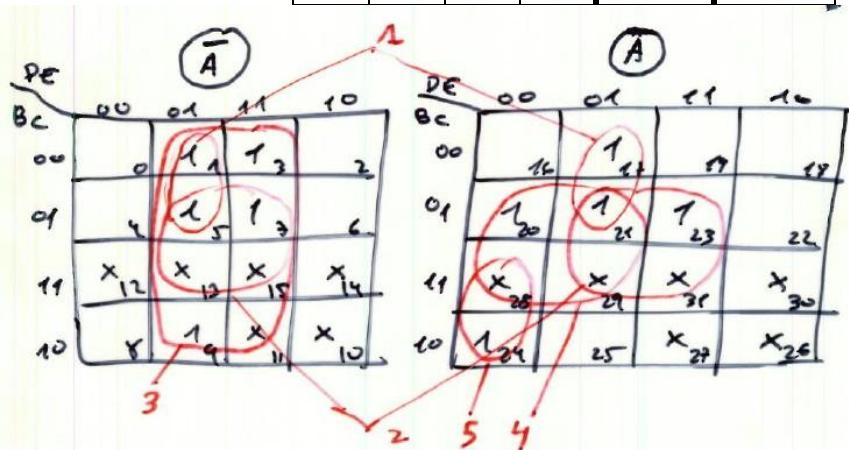
$$\boxed{F = \bar{A}\bar{M} + M\bar{E} + AE}$$

$$\begin{array}{c} ME \\ | \\ \begin{array}{c|c|c|c} & 00 & 01 & 11 & 10 \\ \hline 0 & 1_0 & 1_1 & 3 & 1_2 \\ \hline 1 & 4 & 1_5 & 1_7 & 1_6 \end{array} \end{array}$$

$$F = \bar{A}\bar{M} + AE + M\bar{E}$$

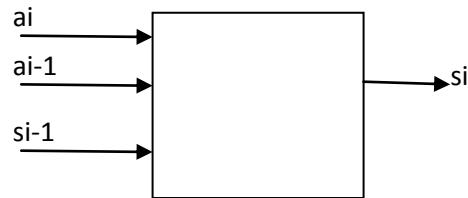
13)

B	C	D	E	F (A=0)	F (A=1)
0	0	0	0	0	0
0	0	0	1	1	1
0	0	1	0	0	1
0	0	1	1	1	0
0	1	0	0	0	1
0	1	0	1	1	1
0	1	1	0	0	0
0	1	1	1	1	1
1	0	0	0	0	1
1	0	0	1	1	0
1	0	1	0	X	X
1	0	1	1	X	X
1	1	0	0	X	X
1	1	0	1	X	X
1	1	1	0	X	X
1	1	1	1	X	X



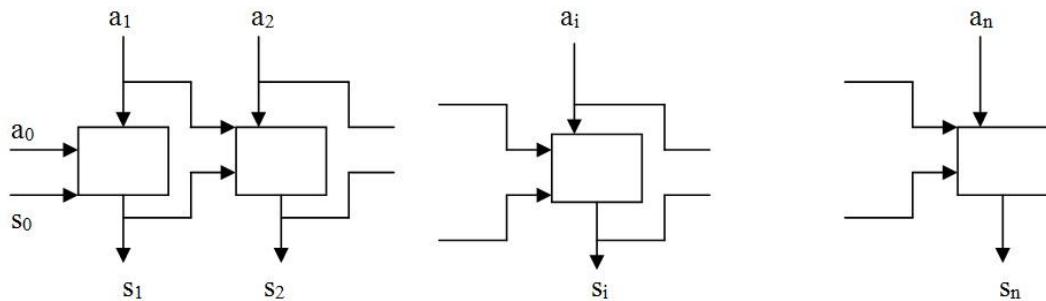
$$F = \bar{B}\bar{D}E + CE + \bar{A}E + A\bar{C}\bar{O} + AB\bar{D}\bar{E},$$

14)



s_{i-1}	a_i	a_{i-1}	s_i
0	0	0	0
0	0	1	1
0	1	0	0
0	1	1	1
1	0	0	1
1	0	1	1
1	1	0	0
1	1	1	0

$$s_i = \overline{s_{i-1}}a_{i-1} + s_{i-1}\overline{a_i}$$



a_0 eta s_0 sarreren balioa zirkuituaren espezifikazioen araberakoak dira.

15)

Sarrera-aldagaiak		Irteera-aldagaiak			
A(Emaztea)	0 = Ados 1 = Aldaranztu				
B(Lehendak)	0 = kontra 1 = Alde	Z	0 = Atzera bota 1 = Onartu da		
C,D,E (Bokalak)	0 = Kontra 1 = Alde				

A	B	C	D	E	Z
0	0	0	0	0	0
0	0	0	0	1	0
0	0	0	1	0	0
0	0	0	1	1	0
0	0	1	0	0	0
0	0	1	0	1	0
0	0	1	1	0	0
0	0	1	1	1	1
0	1	0	0	0	0
0	1	0	0	1	1
0	1	0	1	0	1
0	1	0	1	1	1
0	1	1	0	0	1
0	1	1	0	1	1
0	1	1	1	0	1
0	1	1	1	1	1

A	B	C	D	E	Z
1	0	0	0	0	1
1	0	0	0	1	1
1	0	0	1	0	1
1	0	0	1	1	1
1	0	1	0	0	1
1	0	1	0	1	1
1	0	1	1	0	1
1	0	1	1	1	0
1	1	0	0	0	1
1	1	0	0	1	0
1	1	0	1	0	0
1	1	0	1	1	0
1	1	1	0	0	0
1	1	1	0	1	0
1	1	1	1	0	0
1	1	1	1	1	0

16)

Sarrera-aldagaiak		Irteera-aldagaiak		
X	0 = $T_d < T_f$ 1 = $T_d \geq T_f$	B	0 = Ponpa desaktibatuta 1 = Ponpa aktibatuta	
Y	0 = $T_p < T_f$ 1 = $T_p \geq T_f$	Vp	0 = Panel-balbula itxita 1 = Panel-balbula zabalik	
Z	0 = $T_d < T_p$ 1 = $T_d \geq T_p$	Vg	0 = galdera-balbula itxita 1 = galdera-balbula zabalik	

X	Y	Z	B	Vp	Vg
0	0	0	1	1	1
0	0	1	1	0	1
0	1	0	1	1	0
0	1	1	X	X	X
1	0	0	X	X	X
1	0	1	0	0	0
1	1	0	1	1	0
1	1	1	0	0	0

17)

$$T_i \begin{cases} 0 & P < t_i \\ 1 & P \geq t_i \end{cases}$$

$$f \begin{cases} 0 & \text{Besteak} \\ 1 & t_1 < P < t_2 \text{ o } P \geq t_3 \end{cases}$$



T ₃	T ₂	T ₁	f
0	0	0	0
0	0	1	1
0	1	0	X
0	1	1	0
1	0	0	X
1	0	1	X
1	1	0	X
1	1	1	1

P < t₃; P < t₂; P < t₁ \Rightarrow P < t₁
P < t₃; P < t₂; P \geq t₁ \Rightarrow t₁ < P < t₂
P < t₃; P > t₂; P < t₁ \Rightarrow ezinezkoa
P < t₃; P > t₂; P > t₁ \Rightarrow t₂ < P < t₃
P > t₃; P < t₂; P < t₁ \Rightarrow ezinezkoa
P > t₃; P < t₂; P > t₁ \Rightarrow ezinezkoa
P > t₃; P > t₂; P < t₁ \Rightarrow ezinezkoa
P > t₃; P > t₂; P > t₁ \Rightarrow P > t₃

$$\text{a)} \quad f = \sum(1,7) + k(2,4,5,6) = \overline{T_3}\overline{T_2}T_1 + T_3\overline{T_2}T_1 + k(2,4,5,6)$$

$$f = \prod(0,3).k(2,4,5,6) = (T_3 + T_2 + T_1)(\overline{T_3} + \overline{T_2} + \overline{T_1}).k(2,4,5,6) \quad \text{b)} \quad f = \overline{T_2}T_1 + T_3$$

$$f = (T_1)(T_3 + \overline{T_2})$$

$$\text{c)} \quad f = \overline{\overline{\overline{T_2}}\overline{T_1} + T_3} = \overline{\overline{\overline{T_2}}\overline{T_1}} + T_3 = \overline{\left(\overline{T_2} + \overline{T_1}\right)} + T_3$$

$$f = \overline{(T_1)(T_3 + \overline{T_2})} = \overline{\overline{T_1} + \overline{(T_3 + \overline{T_2})}}$$

18)

A) 18) T.D.= [1.25, 2.6]

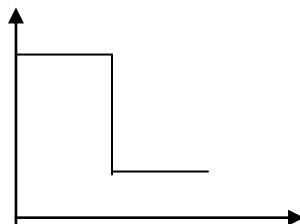
B) $V_{IH} = [2.6, 5]$
 $V_{IL} = [0, 1.25]$

$V_{OH} = [4.6, 4.8]$
 $V_{OL} = [0.2, 0.8]$

C) Zarata-tartea zenbat eta handiagoa izan hobe. Hortaz, zarata-tartea lortzeko adierazpenen arabera:

$$ZT_H = V_{OH} - V_{IH}$$
$$ZT_L = V_{IL} - V_{OL}$$

Zarata-tartea handitzeko nahikoa da V_{IH} txikitzea ZTH kasuan eta handitzekoa V_{IL} ZTL kasuan.



19)

Familia ezberdinak elkar konektatu daitezke kitzikapen ateen irteeren maila logikoak karga-ateen tarte logikoen barne baldin badaude, hau da, $V_{OHmin} > V_{IHmax}$ y $V_{OLmax} < V_{ILmax}$.

Hortaz, konektatu ezin direnak dira TTL-HCMOS eta AS- HCMOS.

Fan-out kitzikapen-atearen irteeren korronte eta karga atearen sarreren korrontearren menpe dago. Alabaina, CMOS ateetan karga atearen sarreren kapazitantziaren menpekoa da. Fan-out-a maila altuan zein baxuan kalkulatu behar da eta murrizgarriena txikiena izango da.

20)A)

0	0	1	0	0	0	1	0	Konbinazioa onartua
0	0	1	1	0	0	0	0	Debekatuta 2 zerra aktibo
0	1	0	0	0	0	0	0	Debekatuta zinta lubrikaziorik gabe
0	1	0	1	0	0	0	0	Debekatuta zinta eta ebaketa
0	1	1	0	0	0	0	0	Debekatuta zinta lubrikaziorik gabe
0	1	1	1	0	0	0	0	Debekatuta 2 zerra aktibo
1	0	0	0	1(0)	0	0	0	Konbinazioa onartua
1	0	0	1	1(0)	0	0	1	Konbinazioa onartua
1	0	1	0	1(0)	0	1	0	Konbinazioa onartua
1	0	1	1	0	0	0	0	Debekatuta 2 zerra aktibo
1	1	0	0	1	1	0	0	Konbinazioa onartua
1	1	0	1	0	0	0	0	Debekatuta zinta eta ebaketa
1	1	1	0	1	1	1	0	Konbinazioa onartua
1	1	1	1	0	0	0	0	Debekatuta dena aktibo

Oharra: M1 funtzioaren arabera ontzat ematen dira parentesi arteko eukerak

$$M_1 = I_1(\overline{I}_2 + \overline{I}_4)(\overline{I}_3 + \overline{I}_4)$$

$$M_2 = I_1 \cdot I_2 \cdot \overline{I}_4$$

$$M_3 = I_3 \cdot \overline{I}_4 \cdot (I_1 + \overline{I}_2)$$

$$M_4 = I_4 \cdot \overline{I}_3 \cdot \overline{I}_2$$

B)

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$$Y = D_0 \overline{S}_2 \overline{S}_1 \overline{S}_0 + D_1 \overline{S}_2 \overline{S}_1 S_0 + D_2 \overline{S}_2 S_1 \overline{S}_0 + D_3 \overline{S}_2 S_1 S_0 +$$

$$D_4 S_2 \overline{S}_1 \overline{S}_0 + D_5 S_2 \overline{S}_1 S_0 + D_6 S_2 S_1 \overline{S}_0 + D_7 S_2 S_1 S_0$$

$$\begin{aligned} M_1(I_1 I_2 I_3 I_4) &= 0 \cdot \overline{I}_1 \overline{I}_2 \overline{I}_3 \overline{I}_4 + 0 \cdot \overline{I}_1 \overline{I}_2 \overline{I}_3 I_4 + 0 \cdot \overline{I}_1 \overline{I}_2 I_3 \overline{I}_4 + 0 \cdot \overline{I}_1 \overline{I}_2 I_3 I_4 + \\ &0 \cdot \overline{I}_1 I_2 \overline{I}_3 \overline{I}_4 + 0 \cdot \overline{I}_1 I_2 \overline{I}_3 I_4 + 0 \cdot \overline{I}_1 I_2 I_3 \overline{I}_4 + 0 \cdot \overline{I}_1 I_2 I_3 I_4 + \\ &1 \cdot I_1 \overline{I}_2 \overline{I}_3 \overline{I}_4 + 1 \cdot I_1 \overline{I}_2 \overline{I}_3 I_4 + 1 \cdot I_1 \overline{I}_2 I_3 \overline{I}_4 + 0 \cdot I_1 \overline{I}_2 I_3 I_4 + \\ &1 \cdot I_1 I_2 \overline{I}_3 \overline{I}_4 + 0 \cdot I_1 I_2 \overline{I}_3 I_4 + 1 \cdot I_1 I_2 I_3 \overline{I}_4 + 0 \cdot I_1 I_2 I_3 I_4 \end{aligned}$$

Helburua da Y = M₁ izatea, hortaz:

$$I_4 = S_0 \quad I_3 = S_1 \quad I_2 = S_2$$

$$D_0 = I_1$$

$$D_1 = I_1$$

$$D_2 = I_1$$

$$D_3 = 0$$

$$D_4 = I_1$$

$$D_5 = 0$$

$$D_6 = I_1$$

$$D_7 = 0$$

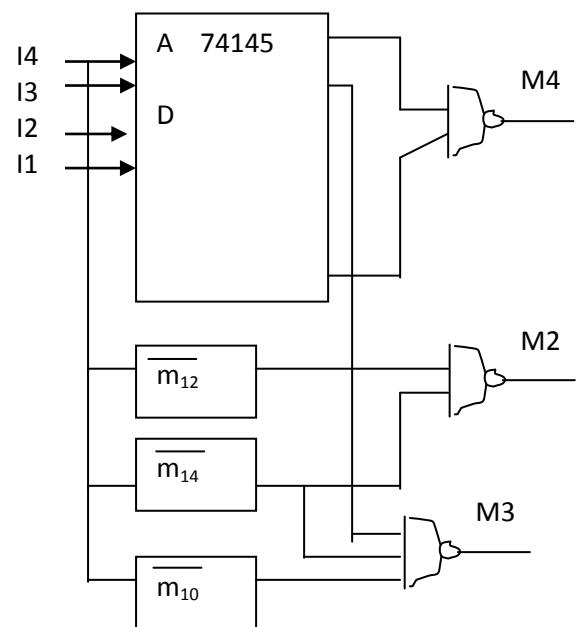
Oharra: Egi-taulatik zuzenean ondoriozta daiteke.

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$$M_2 = \overline{\overline{m_{12}} + m_{14}} = \overline{\overline{m_{12}} \cdot \overline{m_{14}}}$$

$$M_3 = \overline{\overline{m_2} + m_{10} + m_{14}} = \overline{\overline{m_2} \cdot \overline{m_{10}} \cdot \overline{m_{14}}}$$

$$M_4 = \overline{m_1 + m_9} = \overline{m_1} \cdot \overline{m_9}$$



21)

a.1) V_{cc} eta luraren artean impedantzia baxuko bidea osa daiteke eta korronteak integratua izorra dezake.

AND atearen irteera H moduan egon dadin eta NOT L -ra behartzen dituzten konbinazioak, edo alderantziz.

Konbinazio arriskutsua izango litzateke $A = B = 1$ y $C = 0$, adibidez.

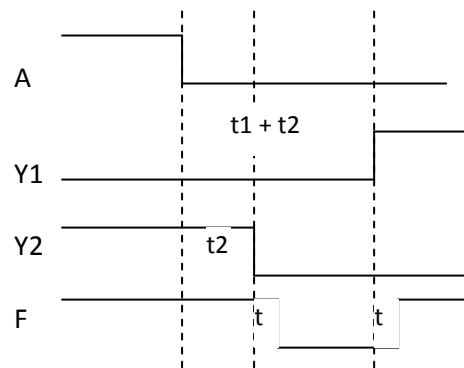
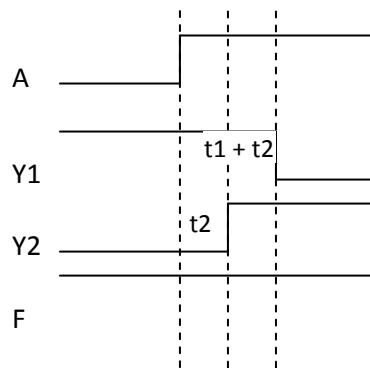
a.2) Kolektore edo drenadore zabaleko atea jarriko genituzke.

Horiek duten ezbeharra da gailu extra behar dutela (Pull-up erresistentzia)

b.1) Logika erredundantea erabiltzen da, hots, behar ez diren atea gehitzen dira irteeran *glitchak* ager ez daitezten.

Zirkituak funtzio hau osatzen du: $F = A'BD + AC = \Sigma(5, 7, 10, 11, 14, 15)$. Beha daiteke A-ren trantsizioetan ($0 \leftrightarrow 1$) arriskkua dagoela $B = C = D = 1$ denean, talde batetik bestera saltoa ematen delako. Hortaz, trantsizio hori egiten duen taldea gehitzen da. Talde hori BCD gaia da eta zirkuituari gehitzen zaio: $\Rightarrow F = A'BD + AC + BCD$

b.2) A-ren bi trantsizioak ($0 \leftrightarrow 1$) arriskutsuak dira $B = C = D = 1$ denean. Alabaina, batek bakarrik sortzen du *glitch*, hau da, $A : 1 \rightarrow 0$ trantsizioak.



b.3) -Gehitu atzerapenak

- Transitorioa eliminatu

22)

Sarrerak:

$dm = 0$ habea ez dago kokatuta

$b_1, b_2, b_3, b_4 = 1$ habea detektatzen dute

$C_i = 1$ C_i biltegira doa

$A = 1$ sentsoreen informazioa ezinezka delako akatsa dago

B4	B3	B2	B1	Dm = 1					Dm = 0				
				C1	C2	C3	C4	A	C1	C2	C3	C4	A
0	0	0	0	0	0	0	0	1	0	0	0	0	0
0	0	0	1	0	0	0	0	1	0	0	0	0	1
0	0	1	0	1	0	0	0	1	0	0	0	0	1
0	0	1	1	0	0	0	0	1	0	0	0	0	1
0	1	0	0	0	0	0	0	1	0	0	0	0	1
0	1	0	1	0	0	0	0	1	0	0	0	0	1
0	1	1	0	0	0	0	0	0	0	0	0	0	1
0	1	1	1	0	0	1	0	0	0	0	0	0	1
1	0	0	0	0	0	0	0	1	0	0	0	0	1
1	0	0	1	0	0	0	0	1	0	0	0	0	1
1	0	1	0	0	1	0	0	1	0	0	0	0	1
1	1	0	0	0	0	0	0	1	0	0	0	0	1
1	1	0	1	0	0	0	0	1	0	0	0	0	1
1	1	1	0	0	0	0	0	0	0	0	0	0	1
1	1	1	1	0	0	0	1	0	0	0	0	0	1

Oharrak:

- 1) Taula honetan, kotxe laburra detektatzeko h2 aktibatuta egon behar da soilik. Kotxe laburra detektatzeko h2-h3 bakarrik aktibatzea edo bi kasuak eraberan (h2 eta h3-h2) erabili duenak ontzat eman dira. Gaizki dagoena, h1-h2-ren aktibazioak kotxe labur moduan hartzea da, kasu honetan ezin dugulako jakin kotxea laburra ala luzea den.
- 2) Taulan “X” gaiak erabili duenak, taula hori ontzat emateko, gai horien esanahia azaldu behar izan du *Karnaught* mapetan sinplifikatzeko erabili izan baditu. Kasu horietan 1 edo 0 moduan aktibatuko bait dute funtzioa.
- 3) Irteera moduan P1,P2,P3, P4 *peaje* mota bakoitzari irteera bat esleitu beharrean, *peaje* bakoitzari kode bat eman ahal zaio. Adibidez: P1 = 00, P1 = 01, P2= 10, P3 = 11, horrela bi irteera izango genituzke.

23)

a)

A	B	C	D	M
0	0	0	0	0
0	0	0	1	0
0	0	1	0	0
0	0	1	1	0
0	1	0	0	0
0	1	0	1	1
0	1	1	0	0
0	1	1	1	0
1	0	0	0	0
1	0	0	1	1
1	0	1	0	0
1	0	1	1	0
1	1	0	0	0
1	1	0	1	1
1	1	1	0	0
1	1	1	1	0

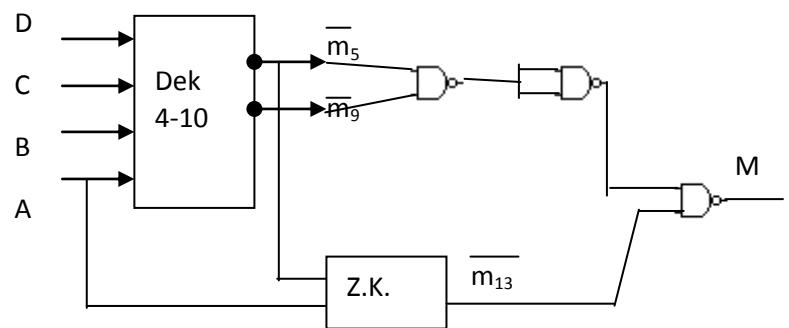
$$M = \overline{CD}(B + A)$$

b) $M = \overline{\overline{BCD} \cdot \overline{ACD}}$

c)

$$M = m_5 + m_9 + m_{13} = \overline{\overline{m_5 + m_9 + m_{13}}} = \overline{\overline{m_5}} \cdot \overline{\overline{m_9}} \cdot \overline{\overline{m_{13}}} = \overline{\overline{m_5}} \cdot \overline{\overline{m_9}} \cdot \overline{\overline{m_{13}}}$$

$$\overline{m_{13}} = \overline{\overline{A \cdot m_5}}$$



24) A) Sarrerak: N, S, E, W = 1 norabidea aktibatua.

Irteerak: a, b, c, d, e, f = 1 segmentua aktibatua.

N	S	E	W	a	b	c	d	e	f
0	0	0	0	0	0	0	0	0	0
0	0	0	1	0	0	0	0	1	1
0	0	1	0	0	1	1	0	0	0
0	0	1	1	X	X	X	X	X	X
0	1	0	0	0	0	0	1	0	0
0	1	0	1	0	0	0	1	1	0
0	1	1	0	0	0	1	1	0	0
0	1	1	1	X	X	X	X	X	X
1	0	0	0	1	0	0	0	0	0
1	0	0	1	1	0	0	0	0	1
1	0	1	0	1	1	0	0	0	0
1	0	1	1	X	X	X	X	X	X
1	1	0	0	X	X	X	X	X	X
1	1	0	1	X	X	X	X	X	X
1	1	1	0	X	X	X	X	X	X
1	1	1	1	X	X	X	X	X	X

b) Funtzioen simplifikazioak:

$$a = N$$

$$b = \overline{SE}$$

$$c = \overline{NE}$$

$$d = S$$

$$e = \overline{NW}$$

$$f = \overline{SW}$$

$$g = D$$

25)

a) Funtzio logikoen simplifikazioa Booleren algebraren hurrengo arauetan oinarritzen dira:

$a + \bar{a} = 1$. Eta $ac + ab = a(c+b)$, hots, faktore komuna ateratzen. Hortaz, funtzio logiko $a \cdot \bar{a} = 0$

baten bi gaiek aldagai baten bakarrik ezberdintzen badira, aldagai hori simplifikagarria izango da eta bi gai horiek kenduko dira bakar bat utziz. Simplifikatu diren bi gai horiek ondoz-ondoko gaiak direla esaten da, hau da, haine arteko ezberdintasun bakarra aldagai bat denean.

Karnaugh tauletan ondoz-ondoko gai horiek ondoz-ondoko laukitxoetan kokatzen dira, bai horizontalki edo baita bertikalki ere. Horrela, erraz ikusgarri dira simplifikagarriak diren gaiak.

b) Zarata handiak dauden lantegian zarata tardea handia duen teknologia beharrezkoa da.

Zarata-tardea kalkulatzen da::

	HCMOS	TTL	LS TTL	S TTL	AS TTL
VNH	1.75	0.4	0.7	0.7	0.7
VNL	0.9	0.4	0.4	0.3	0.3

HCMOS egokiena da.

c) Abiadura-potenzia ezaugarriari dagokionez, baliorik txikiena duena hartu beharko da.

Hori lortzeko atzerapen denborak eta potentziaren disipazioa kalkulatu behar dira:

	A	B	C
Tph (ns)	1.1	4.5	10
C(pJ)	16.5	36	5

Hortaz, C egokiena da.

Maiztasunik handiena har dezakeena A izango da, bere atzerapen denbora txikiena baita.

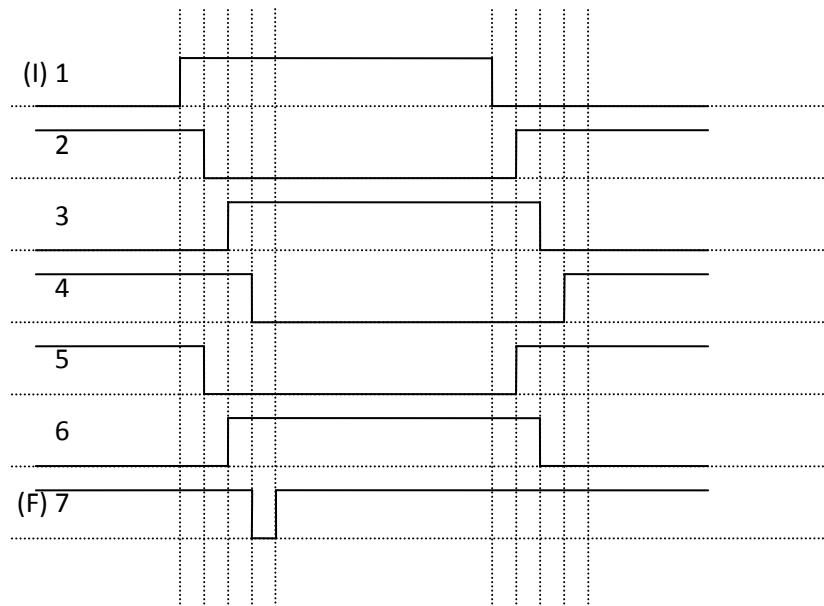
26)

a)

Atzerapen-denbora: 16 ns

Kronograma:

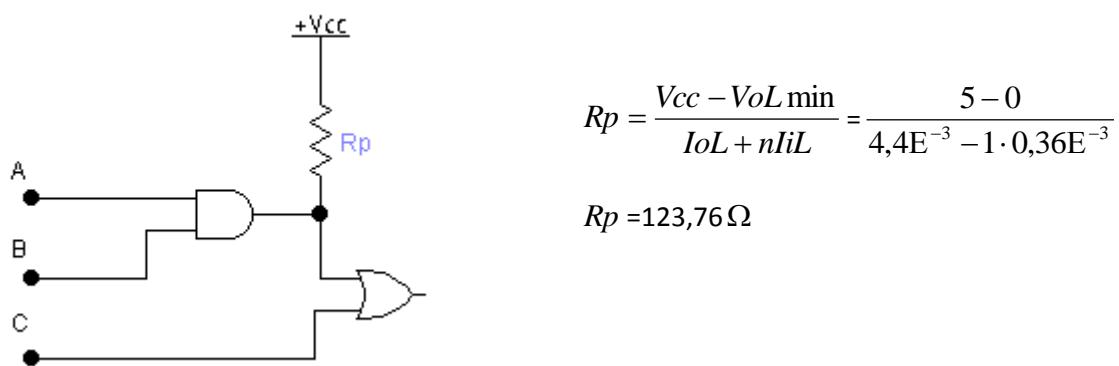
- (I)1: Sarrera da
2, 3, 4: goiko zatiaren ateen irteerak
5, 6: beheko zatiaren ateen irteerak
(F) 7: zirkuituaren irteera.



- a) Zirkuitu 1: ez da baliogarria AND atearen Vohmin ez delako nahikoa Or atea kitzikatzeko
Voh min < Vih min

Zirkutu 2 ondo.

- b) AND atearen Vohmin handitzeko pull-up erresistentzia jarri behar da



27)

a)

Sarrerak				Irteerak	
I3	I2	I1	I0	Z1	Z0
0	0	0	0	0	1
0	0	0	1	0	1
0	0	1	0	0	1
0	0	1	1	1	0
0	1	0	0	1	0
0	1	0	1	0	0
0	1	1	0	0	0
0	1	1	1	0	0
1	0	0	0	1	1
1	0	0	1	1	1
1	0	1	0	1	1
1	0	1	1	X	X
1	1	0	0	X	X
1	1	0	1	X	X
1	1	1	0	X	X
1	1	1	1	X	X

$$Z_1 = (I_3 + I_2 + I_0)(I_3 + I_1 + \bar{I}_0)(\bar{I}_2 + \bar{I}_1)$$

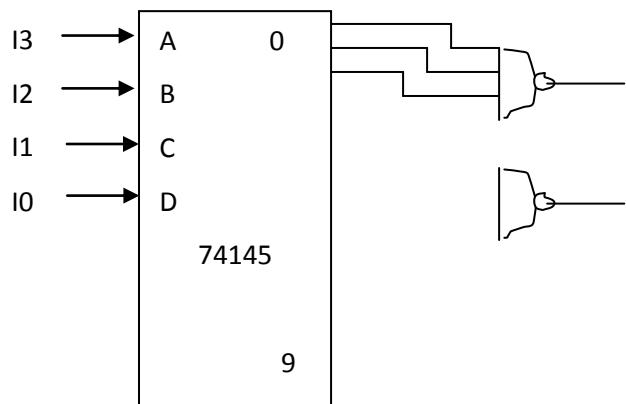
$$Z_0 = \bar{I}_2(\bar{I}_1 + \bar{I}_0)$$

NAND:

$$Z_1 = \overline{\overline{I}_3 \overline{I}_2 \overline{I}_1 \overline{I}_0 \overline{I}_2 \overline{I}_1 \overline{I}_0}$$

$$Z_0 = \overline{\overline{I}_2 \overline{I}_0 \overline{I}_2 \overline{I}_1}$$

b)



$$Z_1 = \overline{M_0 \cdot M_1 \cdot M_2 \cdot M_5 \cdot M_6 \cdot M_7}$$

$$Z_0 = \overline{M_3 \cdot M_4 \cdot M_5 \cdot M_6 \cdot M_7}$$

28)

1) Ez da behar inolako interfazerik sarrera eta irteera tentsio mailak aztertu ondoren

2) Hiru egoeretako atea osatzen duten zirkuitua da, hau da, H, I eta impedantzia altuz (Z) egoerak dituzten atea.

Integratuak lau atea osatzen duten bi taldeetan antolatuta dago, hori dela eta OE (OE1 eta OE2) sarrerarekin talde osoaren impedantza altuko egoera aktibatzen da..

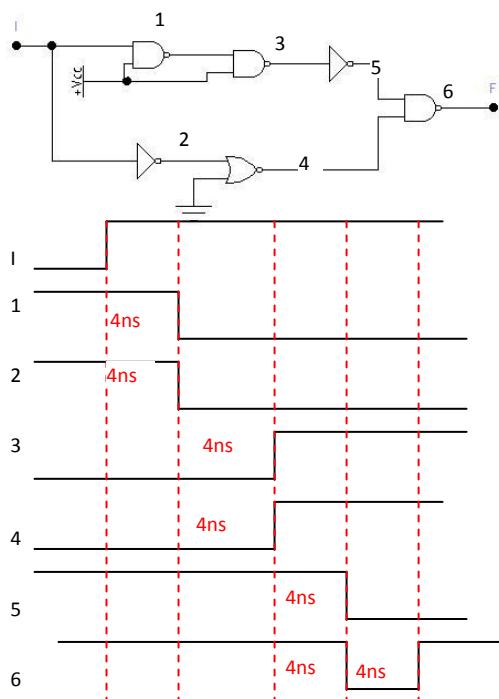
OE1-aren balioa 0 bada, talde horren atea inbertsoreak dira , baina bere balioa 1 denean, impedantzia latuko egoeran egonogo dira.

OE2-aren balioa 1 bada, talde horren atea inbertsoreak dira , baina bere balioa 0 denean, impedantzia latuko egoeran egonogo dira.

Beha daiteke pinek sarrera eta irteera moduan konfigura daitezkelo. Hori dela eta, talde biek ezin dute eraberean funtzionatzen egon, hau da, bata aktibatuta dagoenean bestean impedantzia altuan egon behar da. OE1 funtzionatzen dagoenean 3,4,5,6 pinak sarrerak dira eta 10,11,9,8 pinak irteera.

Zirkuitu hauek data-busera sarrera emateko erabiltzen dira.

3) 4 ate zeharkatu behar dira (biderik luzeena): $4 * 4 = 16\text{ns}$.



4)

0-5	0-15
$V_{OH} = 4,95 \text{ (5)}; V_{OL} = 0$	$V_{OH} = 14,95 \text{ (15)}; V_{OL} = 0,05$
$V_{IH} = 3,5; V_{IL} = 1,5$	$V_{IH} = 11; V_{IL} = 4$
1,5; 15	4 ; 4 hobe

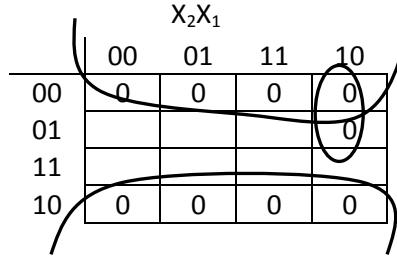
29) x_1 argia piztu; x_2 argia itzalli ; x_3 emergentzia
 x_4 kalean dagoen argia adierazten du.

a)

X_4	X_3	X_2	X_1	L
0	0	0	0	0
0	0	0	1	0
0	0	1	0	0
0	0	1	1	0
0	1	0	0	1
0	1	0	1	1
0	1	1	0	0
0	1	1	1	1
1	0	0	0	0
1	0	0	1	0
1	0	1	0	0
1	0	1	1	0
1	1	0	0	1
1	1	0	1	1
1	1	1	0	1
1	1	1	1	1

X_4X_3

$$L = \Pi (0,1,2,3,6,8,9,11,12)$$



$$L = X_3(X_4 + \bar{X}_2 + X_1)$$

b) 74151A = MUX , 3 aukeratze-sarrera dituena \Rightarrow

$$Y_{MUX} = D_0\bar{C}\bar{B}\bar{A} + D_1\bar{C}\bar{B}A + D_2\bar{C}B\bar{A} + D_3\bar{C}BA + D_4C\bar{B}\bar{A} + D_5C\bar{B}A + D_6CB\bar{A} + D_7CBA$$

$$Y_{MUX} = D_0m_0 + D_1m_1 + D_2m_2 + D_3m_3 + D_4m_4 + D_5m_5 + D_6m_6 + D_7m_7$$

$$L = \sum(4,5,7,12,13,14,15)$$

$$L = \bar{X}_4X_3\bar{X}_2\bar{X}_1 + \bar{X}_4X_3\bar{X}_2X_1 + \bar{X}_4X_3X_2X_1 + X_4X_3\bar{X}_2\bar{X}_1 + X_4X_3\bar{X}_2X_1 + X_4X_3X_2\bar{X}_1 + X_4X_3X_2X_1$$

$$L = \bar{X}_4m'_4 + \bar{X}_4m'_5 + \bar{X}_4m'_7 + X_4m'_4 + X_4m'_5 + X_4m'_6 + X_4m'_7$$

$$L = m'_4(\bar{X}_4 + X_4) + m'_5(\bar{X}_4 + X_4) + m'_7(\bar{X}_4 + X_4) + X_4m'_6$$

$$L = 1.m'_4 + 1.m'_5 + 1.m'_7 + X_4m'_6$$

$$CBA = X_3X_2X_1$$

$$Y_{MUX} = D_0m_0 + D_1m_1 + D_2m_2 + D_3m_3 + D_4m_4 + D_5m_5 + D_6m_6 + D_7m_7$$

$$L = 1.m'_4 + 1.m'_5 + 1.m'_7 + X_4m'_6$$

$$D_0 = 0 \quad D_1 = 0 \quad D_2 = 0 \quad D_3 = 0$$

$$D_4 = 1 \quad D_5 = 1 \quad D_6 = X_4 \quad D_7 = 1$$

